

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Hidekazu SATO, et al.**

Serial Number: **Not Yet Assigned**

Filed: **September 22, 2003**

For: **SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 22, 2003

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark

Office the references listed on the attached PTO-1449.

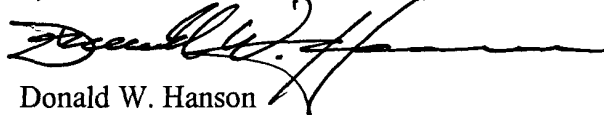
A copy of each of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (3)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 031179	Serial No. New Application
	Applicant(s): Hidekazu SATO, et al.	
	Filing Date: September 22, 2003	Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____	AE		
_____	AF		
_____	AG		
_____	AH		
_____	AI		

OTHER DOCUMENTS

_____	AJ	Patent Abstracts of Japan, Publication No. 5-062991, dated March 12, 1993.		
_____	AK	Patent Abstracts of Japan, Publication No. 10-125691, dated May 15, 1998.		
_____	AL	Patent Abstracts of Japan, Publication No. 11-126781, dated May 11, 1999.		
<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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